

EAST Search History

Ref #	Hit#	Search Query	Obs	Default Operator	Results	Time Stamp
L1	14391	Search187	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L1	99129	mmid444444	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L3	603865	je464 cabotage mmp44 mmp44 mmp44 mmp44 mmp44	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L4	1198572	Ref node	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L5	34716	L3 and L4	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L6	111330	71467 702013 702014	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L7	9515	L3 and L4 and L5	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L8	729	L5 and L7	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45

1/28/2008 7:35:12 PM

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EAST Search History

Ref #	Hit#	Search Query	Obs	Default Operator	Results	Time Stamp
L9	39	L8 and L2	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L10	387	Ref and Grant	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L11	21	L10 and L9	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L12	26	je46 and L9	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L13	12	(Grant and L4) and L6 and L2	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L14	11	L12 and L14	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45
L15	3	je46 and L10 and L12	US-APRARE; USPAT; USOCC; EPC; DEREWENT; BPM_TTB	OR	ON	2008/01/28 18:45

1/28/2008 7:35:12 PM

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gpt.txt

Google Search Results

Jan 19, 2009
Searched for simulation OR emulation circuit functional OR behavior (grouping) OR merging

Searched for simulation OR emulation merge circuit identical OR same OR isomorphic OR class behavior OR function OR results - Viewed 3 results/Viewed 3 results

Not started Automated load determination for partitioned simulation - OR ... - partitions.htm - See 1 more page - Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/patents/655343-claim.html

Not started <http://www.patentstorm.us/patents/655836-description.html> - Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/patents/655836-description.html

Not started <http://www.patentstorm.us/patents/655836-description.html> - Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/patents/655836-description.html

Not started System and method for adaptive partitioning of circuit... - freepatentonline.com - Bookmark: edit - remove

Labels:
Notes:
www.freepatentonline.com/704652.html

Searched for merging circuits same OR identical behavior simulation OR emulation

n. Searched for merging circuits same behavior simulation OR emulation

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Searched for merging circuits same behavior simulation OR emulation gpt.txt

Searched for connection merging circuits pin simulation OR emulation

Searched for port connection merging circuits pin simulation OR emulation

Searched for port connection merging circuits pin

Searched for port connection dynamic merging circuits pin

Searched for port connection dynamic merging circuits

Searched for port connection dynamic merging circuits - Viewed 1 result/Viewed 1 result

Not started A compact multiway merge meter using VLSI linear array... - springerlink.com - Bookmark: edit - remove

Labels:
Notes:
www.springerlink.com/10.1007/s102370120223.pdf

Searched for port

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EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4477001	(port input output pin connector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:36
L2	6450721	connect\$4 link\$4 map\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:41
L3	1	"6553543".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:41